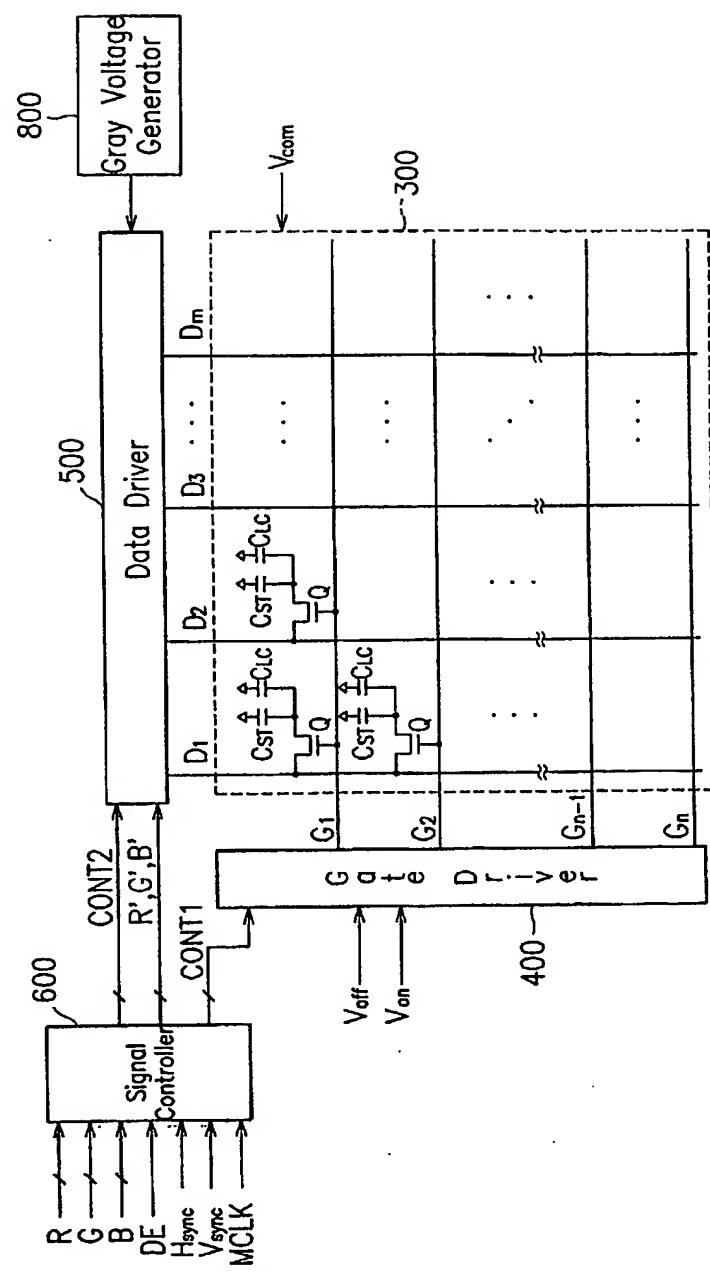


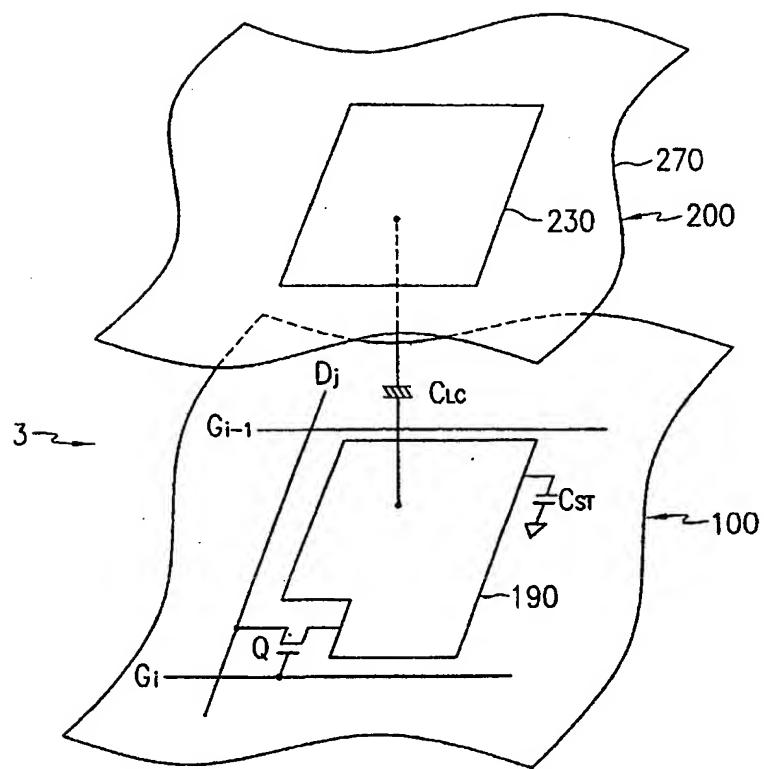
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FIG.1



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FIG.2



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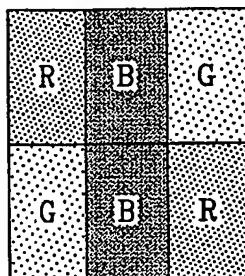
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FIG.3A

R	B	G	R	B	G
G	B	R	G	B	R
R	B	G	R	B	G
G	B	R	G	B	R

FIG.3B



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FIG.4A

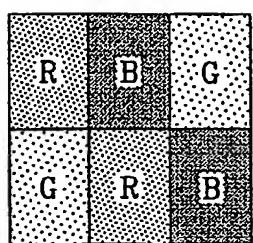
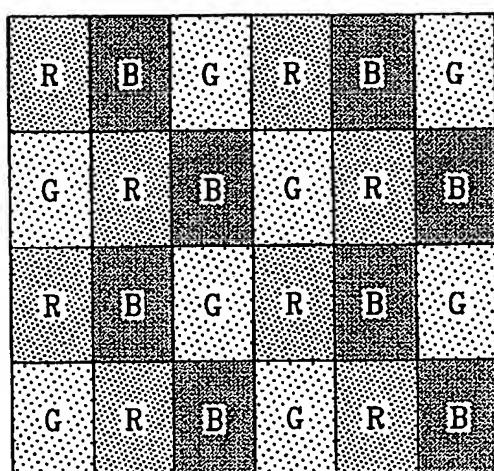


FIG.4B



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FIG.5A

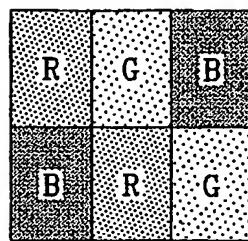
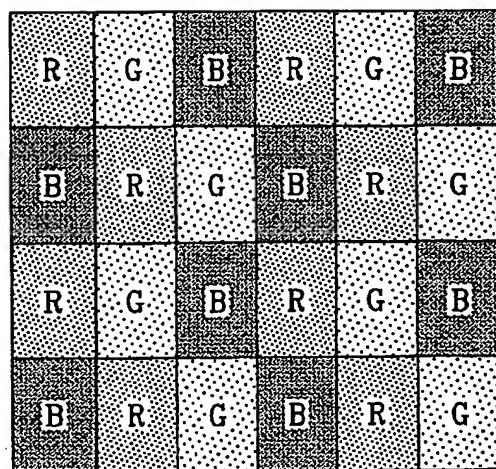


FIG.5B



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FIG.6

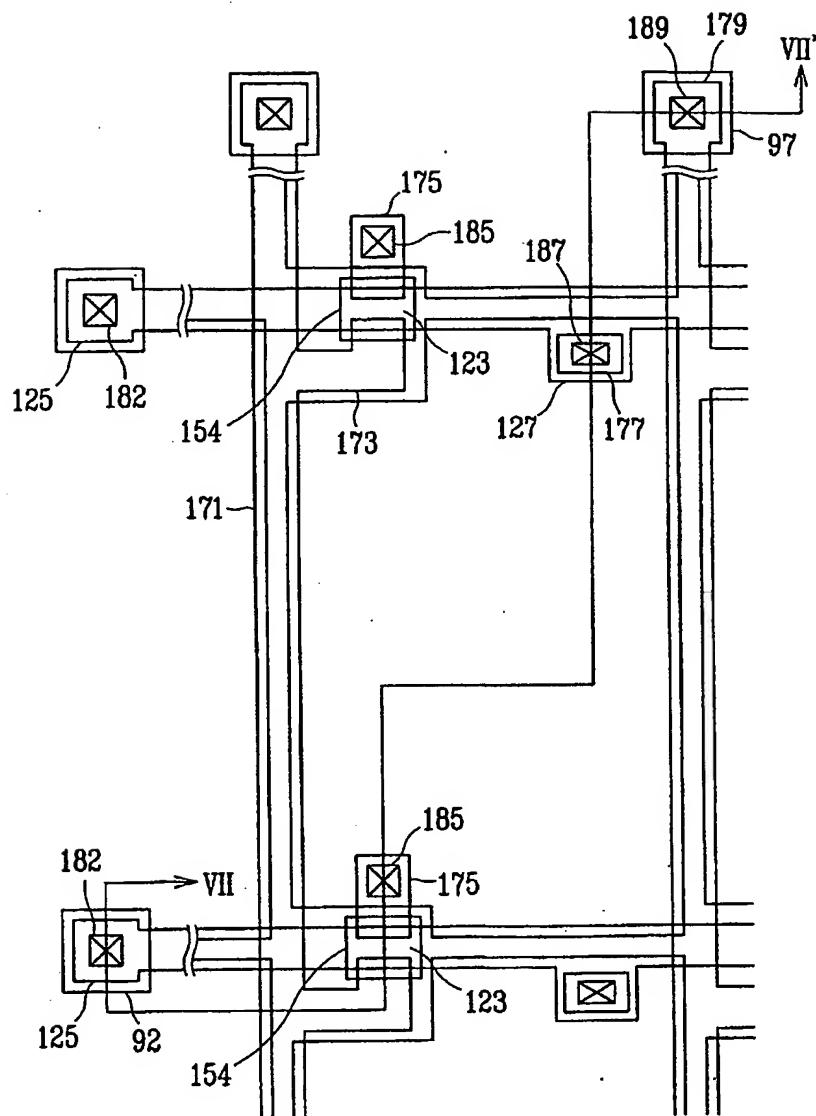
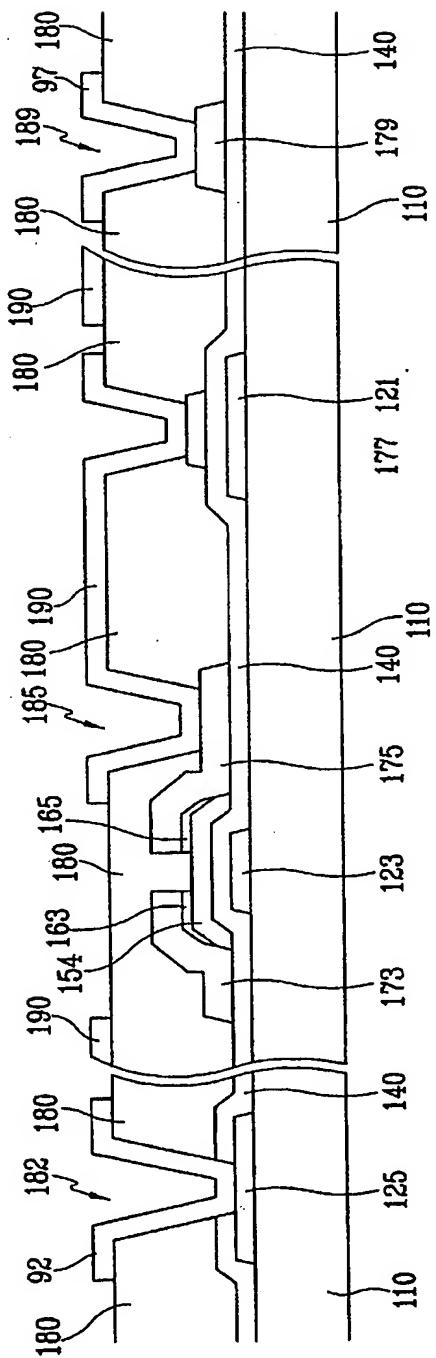


FIG.



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FIG.8

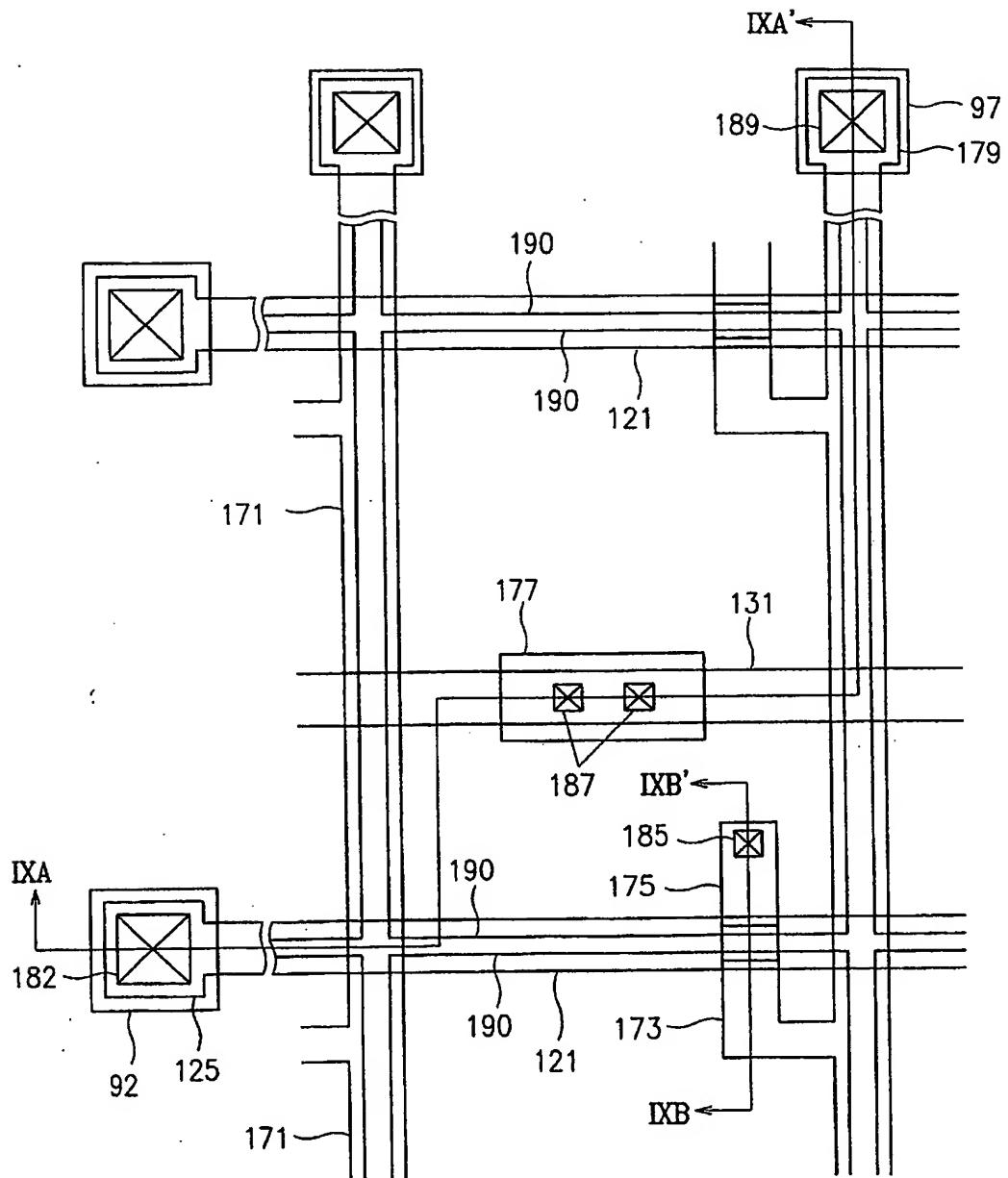
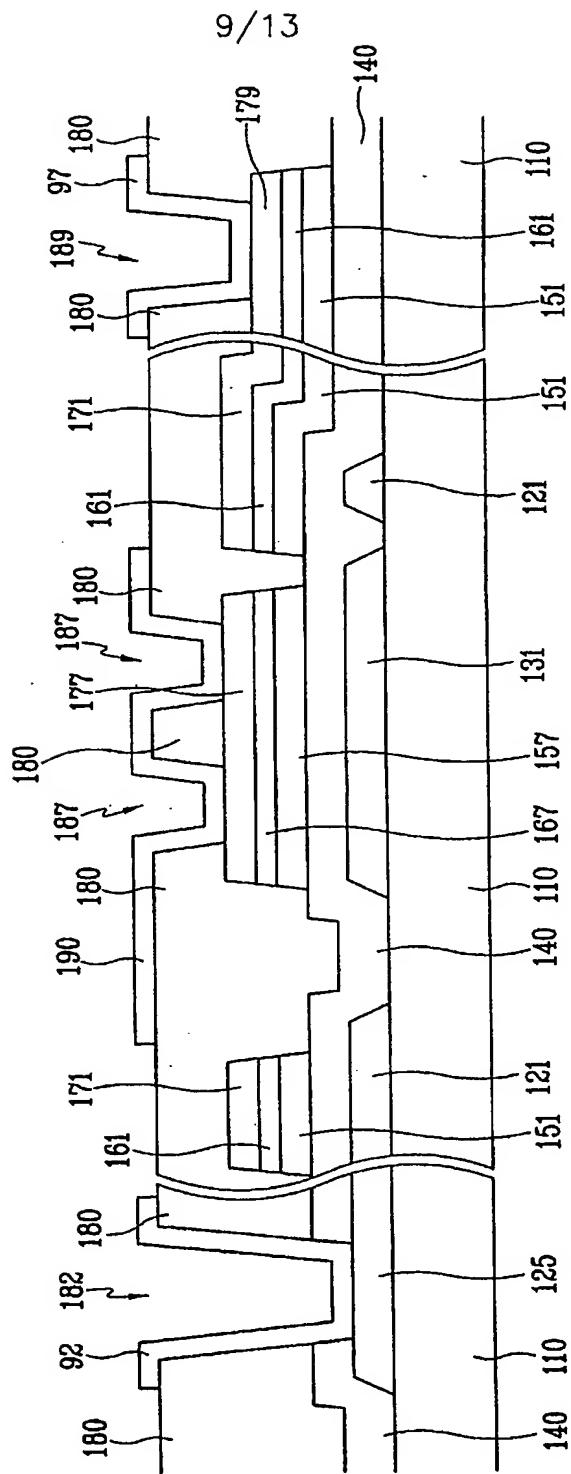


FIG. 9A



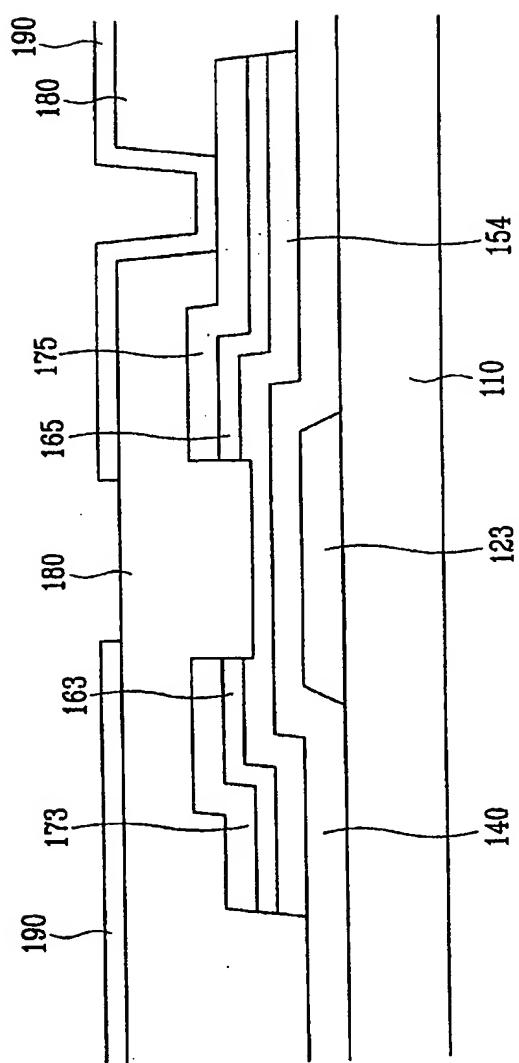
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FIG. 9B



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FIG.10A

R	B	G	R	B	G	
G	B	R	G1	B	R	
R	B	G2	R	B1	G3	
G	B	R	G4	B2	R	

Phase error

FIG.10B

R	B	G	R	B	G	
G	R	B	G1	R	B	
R	B	G2	R	B1	G3	
G	R	B	G4	R	B2	

Phase error

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FIG.10C

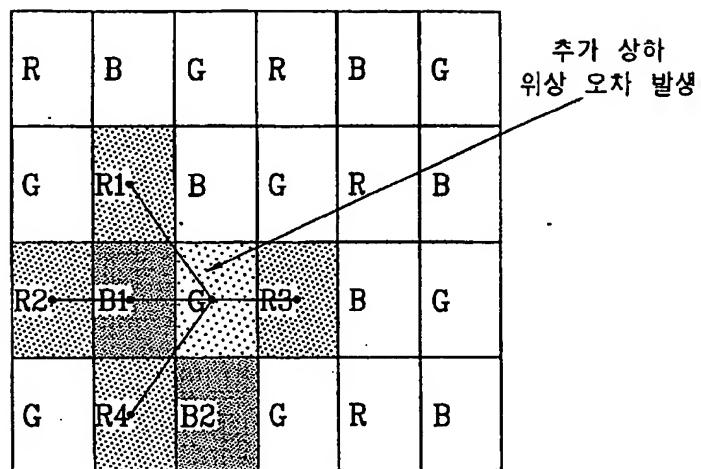
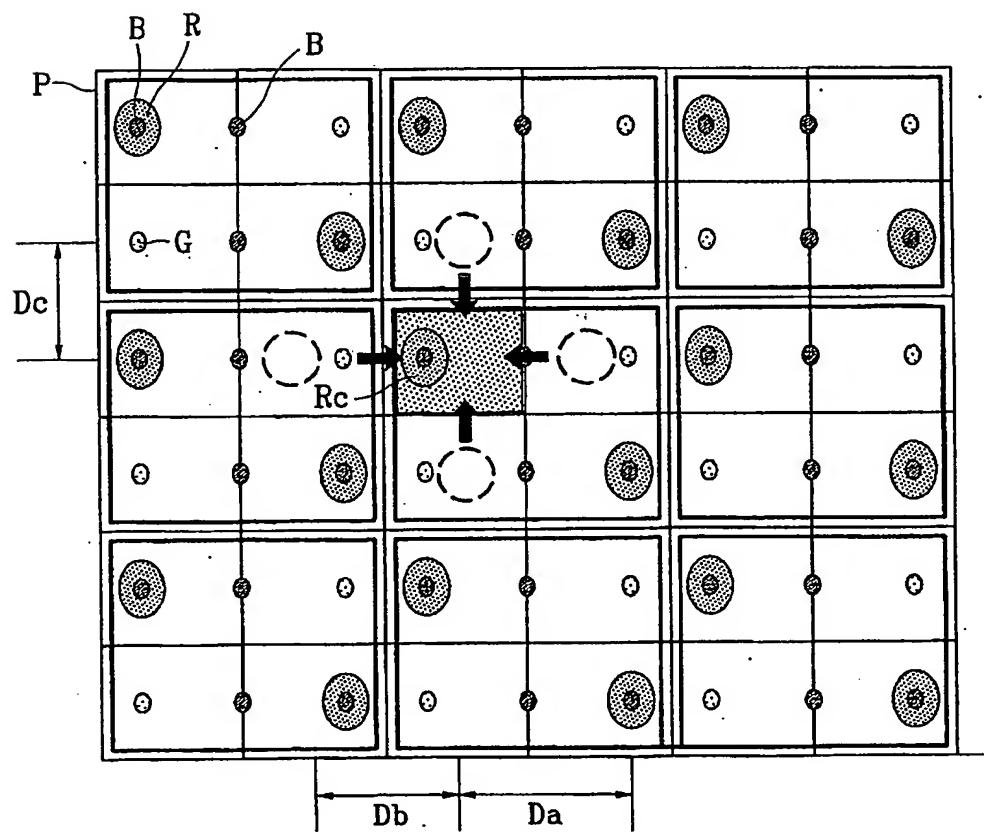


FIG.11



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FIG.12A

## Add Column

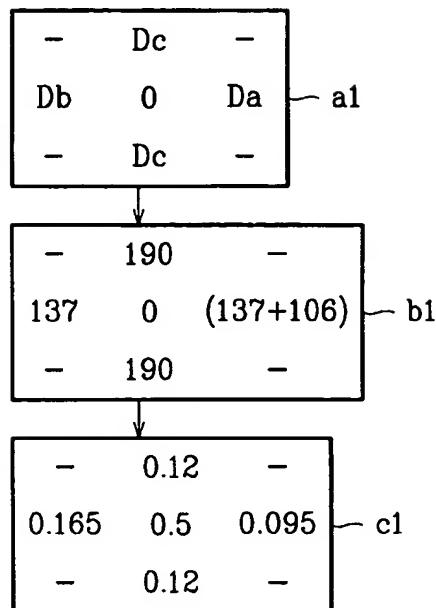


FIG.12B

## Even Column

